



DEPARTMENT OF ELECTRONICS ENGINEERING
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

क्रमांक: DoECE/End-Sem/ 1781 /2025-26

दिनांक: 08-12-2025

End-Semester Exam Schedule (December-2025)

B. Tech. I – Electronics & Communication Engineering
(1st Semester)

Exam Date	Time (09:30 AM to 12:30 PM)
15-12-2025	Semiconductor Physics and Devices(EC101)
16-12-2025	English and Professional Communication(HS110)
17-12-2025	Fundamentals of Computer and Programming(EC103)
18-12-2025	Mathematics-I (MA117)
19-12-2025	Fundamentals of Electrical Engineering(EE110)

B. Tech. I – Electronics and VLSI Engineering
(1st Semester)

Exam Date	Time (09:30 AM to 12:30 PM)
15-12-2025	Semiconductor Physics and Devices(VL101)
16-12-2025	English and Professional Communication(HS110)
17-12-2025	Fundamentals of Computer and Programming(VL103)
18-12-2025	Mathematics-I (MA117)
19-12-2025	Fundamentals of Electrical Engineering(EE110)

M. Tech. I & Ph.D. – Electronics & Communication Engineering
Communication Systems (1st Semester)

Exam Date	Time (09:30 AM to 12:30 PM)
15-12-2025	Advance Optical Communication Systems (ECCS103)
16-12-2025	Information Theory & Coding (ECCS111)
17-12-2025	Advance DSP (ECCS105)
18-12-2025	RF Circuits & Systems (ECCS101)
19-12-2025	Digital VLSI Design (ECVL101)

M. Tech. I & Ph.D. – Electronics & Communication Engineering
VLSI & Embedded Systems (1st Semester)

Exam Date	Time (09:30 AM to 12:30 PM)
15-12-2025	MOS Devices and Technology (ECVL103)
16-12-2025	Embedded Systems (ECVL105)
17-12-2025	Semiconductor IC Technology (ECVL111)
18-12-2025	Testing and Verification of VLSI Circuits (ECVL117)
19-12-2025	Digital VLSI Design (ECVL101)

Note: -

- Students has to **report 15 minutes** before the commencement of the respective examination.
- Exchange of writing materials, stencils, mathematic instruments etc. are strictly prohibited. Carrying Pager, Digital Diary, Cellular Mobile phone, Programmable calculator, Smart watch and other electronic Gadget in the examination hall is also prohibited. Any student found with prohibited materials, disciplinary action will be initiated against the student.
- Do not bring any book, notes or scribbling paper into the examination hall. Do not speak or communicate in any other way and do not take any answer book whiling leaving the examination hall otherwise disciplinary action will be initiated.
- Students will **not be allowed to go outside the Classroom** for the **whole duration of the exam**.


Examination Coordinator, DoECE


HOD, DoECE

Copy to:

- 1) Dy. Registrar (Acad) 2) Chief Exam Coordinator 3) Student NB DoECE
- 4) upload@svnit.ac.in 5) All concerned Departments

In case any query, contact Examination Coordinators, DoECE



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End-Semester Exam (December 2025)

Seating Arrangement:

B. Tech – I (All Subjects)	EC022N U25EV001- U25EV036 (36)	EC021N U25EV037- U25EV063 (27)	EC222N U25EC001- U25EC035 (35)	EC203N U25EC036- U25EC075 (40)	EC106N U25EC076- U25EC115 (40)	EC105N U25EC116- U25EC145 (30)
M. Tech – I	EC022N VLSI (All Students)	EC021N CS (All Students)				

[Signature] 08/12/25
Examination Coordinator, DoECE

[Signature] 8/12/25
HOD, DoECE